EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1874	326/30.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; .DERWENT; IBM_TDB	OR	ON	2007/09/13 09:56
L2	5128	326/37-50.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/13 09:56
L3	35	1 and 2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/13 09:56
L5	30	(US-20050193302-$ or US-20060226867-$ or US-20050194991-$ or US-20070024317-$ or US-20040169525-$ or US-20070188187-$ or US-20060097749-$ or US-20040201405-$ or US-20010015915-$ or US-20060274563-$).did. or (US-7245144-$ or US-7227376-$ or US-6791356-$ or US-6759868-$ or US-6426947-$ or US-7058131-$ or US-7262630-$ or US-5666087-$ or US-6590413-$ or US-5034698-$ or US-6342815-$ or US-7227878-$ or US-5687330-$ or US-6731106-$ or US-5881129-$ or US-5854839-$ or US-6700403-$).did. or (US-2449819-$).did. or (EP-632392-$ or US-6342815-$).did.	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2007/09/13 10:01
L6	8	5 and (test adj mode)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/13 14:19

L7	0	6 and (termination adj resistance)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/13 10:05
L8	7	5 and (termination adj resistance)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/13 10:05
L14	8	("20070057736" "6864704" "6879131" "6590413").pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/13 13:50
L16	7	(US-20070188187-$).did. or (US-7245144-$ or US-6731106-$ or US-6590413-$ or US-6879131-$ or US-6864704-$).did. or (US-20070057736-$).did.	US-PGPUB; USPAT; DERWENT	OR	ON	2007/09/13 14:19
L17	8	5 and (test adj mode)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/13 14:21
L18	0	17 and (on adj resistance)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/13 14:21
L19	1	17 and (resistance with output with buffer)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/13 14:22
S1	2139	switch$3 with (high adj speed adj data)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/11 18:16

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S2	5600	(termination adj (resistor resistance))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/11 16:04
S3	57698	(output adj stage)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/11 16:04
S4	31431	(input adj stage)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/11 16:05
S5	4	S1 and S2 and S3 and S4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/11 16:35
S6	2467	(programmable near2 (resistor resistance))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/11 16:37
S7	4	S1 and S6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/11 17:46
S8	4	("6424200" "5687330").pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/11 17:36
S9	29802	(test$3 adj mode)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/11 17:37

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S10	10	S6 with S9	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/11 17:47
S11	1874	326/30.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/11 17:46
S12	50	S11 and S6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/11 18:11
S13	6	S12 and S9	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/11 17:47
S14	75557	feedback adj loop	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/11 18:12
S15	7	S12 and S14	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/11 18:13
S16	1718	324/537.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/11 18:15
S17	345	324/415.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/11 18:15

S18	34	S17 and (switch$3 adj circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/11 18:17
S19	0	S18 and (high adj speed adj data)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/11 18:17
S20	2	S18 and (high adj speed)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/11 18:16
S21	87	S16 and (switch$3 adj circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/11 18:18
S22	0	S21 and (high adj speed adj data)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/11 18:17
S23	1881	(high adj speed adj data) and (switch$3 adj circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/11 18:18
S24	32	S2 and S23	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/11 18:18
S25	4	S24 and S6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/11 18:21

S26	17	(termination adj resistor) near3 feedback	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/11 18:42
S27	68	(input adj termination adj resistor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/11 18:42
S28	43	(output adj termination adj resistor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/11 18:42
S29	10	S27 and S28	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/12 14:43
S30	1	(Test$3 and switch$3 and circuit and (high adj speed adj data) and integrated and transistor and (test adj mode) and (termination adj resistor) and (output adj stage) and transmission and signal and path and (input adj stage) and reception and feedback and loop).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/12 14:52
S31	2467	(programmable near2 (resistor resistance))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/12 21:47
S33	1	((Integrated adj circuit) and (high adj speed adj data) and transmit$4 and signal and (transmission adj line) and (termination adj resistor) and (output adj impedance) and (reception adj data adj signal adj path) and (input adj stage) and load and control$5 and test and (switching adj circuit) and transistor and (test adj mode) and (output adj stage) and feedback and loop).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/12 22:49

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S34	1	(Integrated adj circuit) and (high adj speed adj data) and transmit$4 and signal and (transmission adj line) and (termination adj resistor) and (output adj impedance) and receiver and load and (switching adj circuit) and transistor and (test adj mode) and feedback	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/12 22:01
S35	745	(Integrated adj circuit) and (switching adj circuit) and (test adj mode)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/12 22:03
S36	6	S31 and S35	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/12 22:49
S37	5600	(termination adj (resistor resistance))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/12 22:10
S38	5	S37 and S35	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/12 22:11
S39	1063	714/733.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/12 22:49
S40	22	S39 and S35	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/12 22:50
S41	1	S40 and S37	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/12 22:50